Application No.: 10/701,045
Reply to Office Action of May 13, 2005

Amendments to the Specification:

Please amend the paragraph labeled [0019] with the following amended paragraph:

[0019] More particularly, the process of self-alignment and dynamic tuning can be understood based on the following balance equation:

$P_{rf.load} + P_{rf,\text{~~tunrer~~}\underline{tuner}} = P_{dc} - P_{diss} + P_{rf.in}$

where $P_{rf.load}$ is the power to the load, here represented in FIG, 1 by resistor Rl;

$P_{rf,\text{~~tunrer~~}\underline{tuner}}$ is the power dissipated in the tuner 22;

P_{dc} is the power fed to the transistor 12;

P_{diss} is the power dissipated in the transistor as represented by the output voltage of the bridge 16 (i.e., the voltage between nodes N1 and N3); and

$P_{rf.in}$ in the input radio frequency (rf)power fed to the gate G of transistor 12.